

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Nicolae Marin and Sridhar Kotikalapoodi
Title: Band-Gap Reference Circuit With High Power Supply Ripple
Rejection Ratio
Application No.: Unknown
Filing Date: Herewith
Examiner: Unknown
Group Art Unit:: Unknown
Confirmation No.: Unknown
Law Office: Sidley Austin Brown & Wood LLP
Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the following documents to the attention of the Examiner for the above-identified patent application. This application has a filing date after June 30, 2003. Therefore, copies of the U.S. patents and U.S. patent application publications listed on the accompanied form PTO-1449 are not enclosed.

A form PTO-1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or

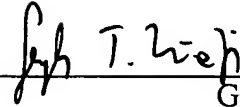
3. an admission that the information listed herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL LABEL NO.:

Respectfully submitted,

EV 305 257 426 US

By:



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November 19, 2003
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